



Department of Electronics and Communication Engineering  
**K.S.R.M COLLEGE OF ENGINEERING**  
**AUTONOMOUS**  
**SECOND COURSE REVIEW COMMITTEE REPORT(CRC) 2021-22**

<b>Department</b>	ECE	<b>Year/Semester</b>	I M.Tech/I
<b>Course Name</b>	RTL Simulation and Synthesis with PLDs Lab	<b>Course Code</b>	1854112

**Committee Members:**

S.No	Name	Role	Disgnation	Signature
1.	Sri Y. Venkateswara Raju	Coordinator	Asst. Professor	
2.	Sri P. Krishna Teja Yadav	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To design various combinational and sequential circuits Using Verilog HDL 2. To design FSM machines, Vending machines. 3. Implement UART/USART in Verilog.	1. Students are facing problems in designing Sequential circuits 2. Some of the students are absent for the labs	1. The availability of the licenced Xilinx Vivado software. 2. The easy way of Implementing the experiments in the hardware.	1. Difficulty to understand the usage of the test bench in application point of view. 2. Difficult to identify the components and their connection from the students point of view.	To overcome this problem a students are assigned to write the different logic for the same circuit.  <b>Suggestions from CRC -1 report are implemented</b>
Sri P. Krishna Teja Yadav	Sri Y. Venkateswara Raju	Dr.G.Hemalatha		
Member	Coordinator	HOD		

Professor & H.O.D.  
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SECOND COURSE REVIEW COMMITTEE REPORT(CRC) 2021-22

Department	ECE	Year/Semester	I M.Tech/I
Course Name	RTL Simulation and Synthesis with PLDs	Course Code	1854101

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Sri Y.Venkateswara Raju	Coordinator	Asst. Professor	
2.	Smt. S. Sharmila Banu	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. Develop the Verilog HDL to design a digital circuit. 2. Verify the functionality of the digital designs using PLDs. 3. Understand the Static Timing Analysis and clock issues in digital circuits .	1. Students are faced difficulty to understand the Verilog HDL 2. Some of the students are irregular.	<b>1. The basics of the subject are studied in UG Level.</b>	1. Difficult to understand the concepts from the students point of view.  Difficulty to understand the concept Timing Analysis and clock issues in digital circuits.	The Learning sessions will be conducted to avoid the difficulty. <b>Suggestions from CRC -1 report are implemented</b> <b>To make the students to understand the subject easily.</b>

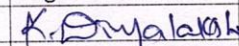

 Smt. S. Sharmila Banu	 Sri Y.Venkateswara Raju	 Dr.G.Hemalatha
Member	Coordinator	HOD

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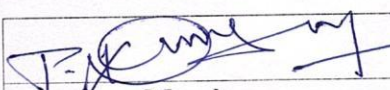
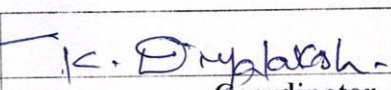
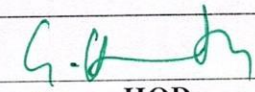
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Department	ECE	Year/Semester	M.Tech I/I
Course Name	CPLD, FPGA architectures and applications.	Course Code	1854111

**Committee Members:**

S.No	Name	Role	Designation	Signature
1.	Smt.K.Divyalakshmi	Coordinator	Asst.Prof	
2.	Sri.P.Krishna teja yadav	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
<p>1. To check the Uniform Coverage of syllabus according to the External Examinations.</p> <p>2. To overcome the shortcomings from students' point of view, in previous meetings like videos.</p>	<p>1 Student wants to listen to teaching in E-class mode and black boards teaching because they understand the topics elaborately.</p> <p>2. The concerned faculty identified few of them are not because of irregularity of attending classes.</p> <p>3. Students Requested for site visit and internship in point of project planning.</p> <p>4. Prescribed Text book for Acts is not available in the Central Library</p>	<p>1. Usage of <b>E Content, Photographs and YOUTUBE</b> videos for explaining each and every topic by This student is able to understand clearly.</p> <p>2. The related lecture notes hand over before commencement of topics to be covered</p>	<p>1. Difficult to understand the FPGA, CPLD Architectures.</p> <p>2. Difficult to Understand the Power and clock distribution terminology.</p>	<p>1. To overcome the above weaknesses, visit a real time ongoing project to understand clearly.</p> <p>2. Explaining topics showing photographs and with all necessary components.</p> <p>3. Exercise few problems in design CPLD, FPGA Architectures.</p>

 Member	 Coordinator	 HOD
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Department	ECE	Year/Semester	I M.Tech/I
Course Name	Design for testability	Course Code	1854107

**Committee Members:**

S.No	Name	Role	Disgnation	Signature
1.	Smt. S. Sharmila Banu	Coordinator	Asst. Professor	
2.	Sri Y.Venkateswara Raju	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To analyze the digital circuits with the presence of faults. 2.To generate the test patterns. 3.To understand the concept of controllability and observability. 4.To determine the built in self test.	1. Students are new to the course and for finding the faults in the digital circuits. 2. Some of the students are irregular.	<b>1. The basics of the subject are studied in UG Level.</b> <b>2.To know the different ways to find the faults.</b>	1.Difficult to understand fault finding - from the students point of view..  Difficulty to understand the concept of controllability and observability	The Learning sessions will be conducted to avoid the difficulty.

 Sri Y.Venkateswara Raju	 Smt. S. Sharmila Banu	 Dr.G.Hemalatha
Member	Coordinator	HOD

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<b>Department</b>	ECE	<b>Year/Semester</b>	<b>M. Tech I Sem</b>
<b>Course Name</b>	Research Methodology & IPR	<b>Course Code</b>	<b>1854103</b>

S.No	Name	Role	Designation	Signature
1.	Sri A. Valli Bhasha	Coordinator	Assistant Professor	
2.	Sri P. Krishna Teja Yadav	Member	Assistant Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To give an overview of the research methodology and explain the technique of defining a research problem	1. Students facing difficulty in understanding various research designs and their characteristics	1. Students know literature search, its review, developing theoretical and conceptual frameworks and writing a review.	1. Student must have basic concepts for understanding this subject	1. To take extra classes to revise the concepts
2. The main objective of intellectual property law is to encourage innovation and to provide incentives.	2. The concerned faculty identified few students are not attending regularly.	2. Students know patents, Trademark, Geographical Indications, Industrial designs etc..	2. Difficult in understanding the concepts	2. To utilize e-content for explaining the difficult topics.

Member	Coordinator	Professor & H.O.D. HOD

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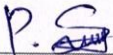
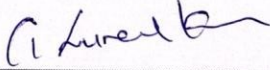
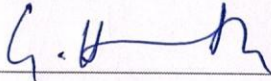
<b>Department</b>	ECE	<b>Year/Semester</b>	II/III
<b>CourseName</b>	ANALOG CIRCUITS	<b>CourseCode</b>	2004303

**CommitteeMembers:**

S.No	Name	Role	Designation	Signature
1	GSuneelKumar	Coordinator	Asst.Prof	
2	P Swetha	Member	Asst.Prof	
3	S Sharmila Banu	Member	Asst. Prof	

Purpose /Objective	ProblemsIdentified	ResultAnalysis		Suggestions forImprovement
		Strength	Weakness	

<p>1. To impart knowledge on Transistor hybrid models.</p> <p>2. To Give basic information in frequency response of amplifiers.</p> <p>3. To teach the characteristics of oscillator circuits.</p>	<p>1. Students are lacking the fundamentals.</p> <p>2. Some of the students are lacking capacitor charging and discharging curve details.</p>	<p>1. The students are aware of Small signal operation of amplifiers.</p> <p>2. The students as acknowledge of transistor operation.</p>	<p>1. Lack of awareness in Cascading techniques of amplifiers.</p> <p>2. Difficulty to understand the concepts of tuned amplifiers.</p>	<p>1. To overcome this problem an introduction to the various cascading techniques is taken.</p> <p>2. Explaining the basics of frequency tuning.</p>
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<p><b>Member</b></p>	<p><b>Coordinator</b></p>	<p><b>HOD</b></p>

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Department	ECE	Year/Semester	II/III
Course Name	Simulation Lab	Course Code	2004305

Committee Members:

S.No	Name	Role	Designation	Signature
1.	P.Lokeshwara Reddy	Coordinator	Assistant Professor	
2.	P.Subbarayudu	Member 1	Assistant Professor	
3.	S.Munawar Ali	Member 2	Assistant Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
<ol style="list-style-type: none"><li>To incorporate the suggestions made by the CRC with respect to first CRC.</li><li>To improve the understand ability of the slow learning student.</li><li></li></ol>	<ol style="list-style-type: none"><li>The concerned faculty identified list slow learning students and the students who are not attending the lab sessions regularly.</li><li>Some of the students are irregular to the lab.</li><li>Some of the students requested for board teaching with examples for effective learning of concepts.</li></ol>	<ol style="list-style-type: none"><li>Explaining the programs using white board and marker</li><li>Utilizing e-content &amp; Power point presentations for explaining the topics</li><li>Faculty is keenly observing the students in the Lab.</li></ol>	<ol style="list-style-type: none"><li>Lack of basics regarding signals and systems.</li><li>Difficult in understanding the concepts.</li></ol>	<ol style="list-style-type: none"><li>To take the extra sessions to the slow learners and irregular students.</li><li>Conduct more practice sessions.</li><li>To take extra sessions to revise the programs.</li></ol>

Member 1

Member 2

Coordinator

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Department	ECE	Year/Semester	II/III
Course Name	Analog Circuits Lab	Course Code	1804408

Committee Members:

S.No	Name	Role	Designation	Signature
1.	M. Prabhakar	Coordinator	Asst.Prof	
2.	Dr.P.Giri Prasad	Member	Asst.Prof	
3.	Miss. Swetha	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To conduct awareness for the students on the electronic circuit's experiments.  2.To analyse and practice various Electronic circuits  3. To overcome the short comings of the students	1. Students want the demonstration the experiments which are going to be dealt by using some YouTube videos .  2. Some of the students are absent for the labs	<b>1.The availability of different amplifiers hardware kits and software tools</b>  <b>2.The easy way of explaining the experiments to the students both hardware and simulation.</b>	1. Lack of awareness in the fundamentals of electronic circuits 2. Difficulty to understand the usage of the experiments in application point of view.	1. To overcome this problem a demo will be conducted using some YouTube videos for all the experiments before the conduction of the lab.  2. Explaining the applications of each experiment and allow the students to mention in the records.

 Member	 Member	 Coordinator	 HOD Professor & M.O.D. Department of E.C.E.
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<b>Department</b>	ECE	<b>Year/Semester</b>	III B.Tech/V
<b>Course Name</b>	Digital IC Applications	<b>Course Code</b>	1804505

**Committee Members:**

S.No	Name	Role	Disgnation	Signature
1.	Sri Y. Venkateswara Raju	Coordinator	Asst. Professor	
2.	Miss.P. Swetha	Member	Asst. Professor	
3.	Kavitha	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
<p>1. <b>Understand</b> CMOS, Bipolar logic families and fundamentals of <b>Verilog</b> HDL Programming.</p> <p>2. <b>Apply</b> the concepts of <b>Verilog</b> HDL for modeling and simulation of digital logic circuits</p>	<p>1. Students are new to the CMOS design based circuits.</p> <p>2. Some of the students are week in digital circuits and Hardware Description Language.</p>	<p>1. <b>The students learnt the basics of CMOS Circuits design.</b></p> <p>2. <b>the concepts of designing CMOS circuits based on digital systems are explained using PPT and verilog concepts are practically shown and the simulation process for easy understanding</b></p>	<p>1. Difficult to analyze the design of circuits using CMOS-from the students point of view..</p> <p>Difficulty to understand the Verilog concepts</p>	<p><b>Suggestions from CRC -1 report are implemented</b></p>

 Miss.P. Swetha	 Sri Y. Venkateswara Raju	 Dr.G.Hemalatha
Member	Coordinator	HOD

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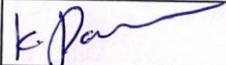
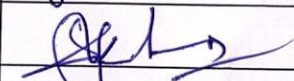
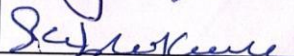


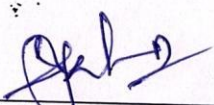
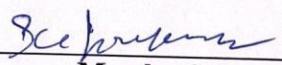
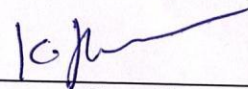
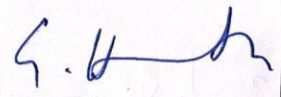
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<b>Department</b>	<b>ECE</b>	<b>Year/Semester</b>	<b>III/V</b>
<b>Course Name</b>	<b>Antennas &amp; Wave Propagation</b>	<b>Course Code</b>	<b>1804506</b>

**Committee Members:**

S.No	Name	Role	Designation	Signature
1.	K.Pavan Kumar	Coordinator	Asst.Prof	
2.	S.Jabeen	Member	Asst.Prof	
3.	S.sudheer Kumar	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
<p>1. To check the uniform coverage of syllabus as per the lesson plan.</p> <p>2. To overcome the shortcomings from student point of view.</p>	<p>1. Students may feel difficulty in derivations.</p> <p>2. Difficulty in analyzing the radiation characteristics of different antennas.</p>	<p>1. Command on fundamentals of mathematics.</p> <p>2. More attention is given on weak students.</p> <p>3. Usage of LMS tools.</p>	<p>1. Difficult to understand derivations.</p> <p>2. Difficult to solve problems</p>	<p>1. Introduction classes are required on fundamentals.</p> <p>2. More number of problems is needed to be solved.</p>
 Member1	 Member2	 Coordinator	 HOD	

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**SECOND COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22**

Department	ECE	Year/Semester	III/ V
Course Name	Microprocessors & Microcontrollers	Course Code	1804501

**Committee Members:**

S.No.	Name	Role	Designation	Signature
1	Sri R.V. Sreehari	Coordinator	Assoc. Professor	
2	Dr.S.L. Pratapa Reddy	Member 1	Assoc. Professor	
3	Kavitha	Member 2	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To incorporate the suggestions made by the CRC with respect to first CRC.	1. Students are facing difficulty in understanding Programming concepts.	1. Video lectures are provided to students for revising the pre-requisite topics.	1. Syllabus is little bit vast at this level.	1. To take tutorial classes to revise the problems.
2.To improve the understandability of the slow learning student.	2. The concerned faculty identified list slow learning students and the students who are not attending the classes regularly.	2. The related notes and study material is distributed to students for revising.	2. Difficult in understanding the concepts.	2. To take the remedial classes to the slow learners and irregular students.
3. To assess the understandability of the student.	3. Faculty identified few students who were not active in the classroom.	3. Faculty is keenly observing the students in the classroom.	3. Few students were not active in the classroom.	3. Conduct Quizzes and slip tests to assess the students.
4.To identify and discuss content beyond the syllabus.	4. Students Requested for more practical applications.	4. Subject having practical significance in modern applications.	4. Students are not familiar with modern applications.	4. To take extra hours to discuss the practical applications.

Member 1

Member 2

Coordinator

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<b>Department</b>	ECE	<b>Year/Semester</b>	III/V
<b>Course Name</b>	Computer Organization	<b>Course Code</b>	1804503

**Committee Members:**

S.No	Name	Role	Designation	Signature
1.	P.Lokeshwara Reddy	Coordinator	Assistant Professor	
2.	A. Sanjeeva Reddy	Member 1	Assistant Professor	
3.	Kavitha	Member 2	Assistant Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To incorporate the suggestions made by the CRC with respect to first CRC. 2. To improve the understand ability of the slow learning student. 3. To classify different peripheral devices 4. To compare different memory units	1. The concerned faculty identified list slow learning students and the students who are not attending the classes regularly. 2. Some of the students are irregular to the classes. 3. Some of the students requested for board teaching for effective learning of concepts.	1. Usage of <b>Pen tablet</b> for explaining each and every topic. 2. The related topic material and PPTs are sent before commencement of topics to be covered. 3. Conducting of assignments and quizzes frequently.	1. Lack of basics regarding digital system design. 2. Difficult in understanding the concepts.	1. To take the remedial classes to the slow learners and irregular students. 2. To take tutorial classes to revise the syllabus.

Member 1

Member 2

Coordinator

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Department	ECE	Year/Semester	III/V
Course Name	Analog and Digital IC Lab	Course Code	1804508

**Committee Members:**

S.No	Name	Role	Disgnation	Signature
1.	Smt S.Sharmila Banu	Coordinator	Asst. Professor	
2.	Miss S.Jabeen	Member	Asst. Professor	
3.	Sri M.Prabhakar	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1.To create awareness for the students on the experiments of analog and digital IC applications..  2.To finish the experiments with in time  3. To overcome the short comings of the students	1.students are new the software xilinx..  2. Some of the students are absent for the labs	<b>1.The availability of the xilinx software .</b>  <b>2.The easy way of explaining the programmes to the students through the basic subject DSD .</b>	1.Difficult in finding the errors in the code- from the students point of view..  Difficulty to understand the usage of the testbench in application point of view.	To overcome this problem a students are assigned to write the different logic for the same circuit.  Explaining the applications of each programme and allow the students to mention in the records.

 S.Jabeen Member	 S.Sharmila Banu Coordinator	 Dr.G.Hemalatha HOD Professor & H.O.D. Department of E.C.E.
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Department	ECE	Year/Semester	VSem
Course Name	DIGITAL SIGNAL PROCESSING	Course Code	1804502

Committee Members::

S.No	Name	Role	Designation	Signature
1	Sri P .SUBBARAYUDU	Coordinator	Asst. Professor	
2	Dr. M.V. NARAYANA	Member	Professor	
3	Smt. HIMAJA REDDY	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To give an overview of different Different Transformations(Z,DFT&FFT) And Filters with real time examples  2.FiltersDesining for real time Applications is much needed.	1. Students facing difficulty in understanding various Transformations and design of Filers and related problems  2. The faculty identified few students are lagging to understand the designing Filters.	1. Students must have the basic knowledge in mathematical functions  2. The real time applications with solutions are explained to boost the confidence of the students.	1. Student are not aware of practical applications of Filters  2. Difficult in understanding the basic Realizations	1. To take extra classes to revise the concepts and solve problems  2. Revision of concepts may improve the understanding ability.

Member	Member	Coordinator	Prof. HOD, N.O.D.

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Department of Electronics and Communication Engineering  
K.S.R.M COLLEGE OF ENGINEERING  
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SECOND COURSE REVIEW COMMITTEE REPORT(CRC) 2021-22

Department	ECE	Year/Semester	III/V
Course Name	Linear and Digital IC Applications Lab	Course Code	2004405

Committee Members:

S.No	Name	Role	Disgnation	Signature
1.	Smt S.Sharmila Banu	Coordinator	Asst. Professor	
2.	Miss P. Swetha	Member	Asst. Professor	
3.	Sri P. SubbaRayudu	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1.To create awareness for the students on the experiments of analog and digital IC applications..  2.To finish the experiments with in time  3. To overcome the short comings of the students	1.students are new the software xilinx..  2. Some of the students are absent for the labs	1.The availability of the xilinx software .  2.The easy way of explaining the programmes to the students through the basic subject DSD .	1.Difficult in finding the errors in the code- from the students point of view..  Difficulty to understand the usage of the testbench in application point of view.	To overcome this problem a students are assigned to write the different logic for the same circuit.  Explaining the applications of each programme and allow the students to mention in the records.

Miss P. Swetha  Member	S.Sharmila Banu  Coordinator	Dr.G.Hemalatha  HOD Professor & H.O.D.
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**Department of Electronics and Communication Engineering**

**SECOND COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22**

<b>Department</b>	ECE	<b>Year/Semester</b>	III/ V
<b>Course Name</b>	Microprocessors & Microcontrollers I.ab	<b>Course Code</b>	1804507

**Committee Members:**

S.No.	Name	Role	Designation	Signature
1	Sri G A Sanjeeva Reddy	Coordinator	Asst. Professor	
2	Sri S Munavar Ali	Member 1	Asst. Professor	
3	Sri Y Venkateswara Raju	Member 2	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To incorporate the suggestions made by the CRC with respect to first CRC.	1. Students are facing difficulty in understanding Programming concepts.	1. Providing individual kits to the students.	1. Lack of programming skills.	1. To take extrasessions to revise the programs.
2.To improve the understandability of the slow learning student.	2. The concerned faculty identified list slow learning students and the students who are not attending the Lab sessions regularly.	2. Explaining the programs using white board and pen.	2. Difficult in understanding the programs.	2. To take the extra sessions to the slow learners and irregular students.
3. To assess the understandability of the student.	3. Faculty identified few students who were not active in the Lab.	3. Faculty is keenly observing the students in the Lab.	3. Few students were not active during the Lab sessions.	3. Conduct more practice sessions.
4.To identify and discuss content beyond the syllabus.	4. Students Requested for more practical applications.	4. Subject having practical significance in modern applications.	4. Students are not familiar with modern applications.	4. To discuss the more interfacing circuits.

Member 1

Member 2

Coordinator

Professor & H.O.D.  
HOD  
Department of E.C.E.  
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**SECOND COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22**

<b>Department</b>	ECE	<b>Year/Semester</b>	III/ V
<b>Course Name</b>	Analog communications	<b>Course Code</b>	1804504

**Committee Members:**

**Date: 13/12/2021**

S.No.	Name	Role	Designation	Signature
1	Dr. P. Giri Prasad	Coordinator	Assit. Professor	
2	Dr. D. Arun Kumar	Member 1	Assoc. Professor	
3	Himaja Reddy	Member 2	Asst. Professor	

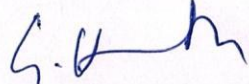
Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To incorporate the suggestions made by the CRC with respect to first CRC.	1. Students are facing difficulty in understanding Programming concepts.	1. Video lectures are provided to students for revising the pre-requisite topics.	1. Syllabus is little bit vast at this level.	1. To take tutorial classes to revise the problems.

2.To improve the understandability of the slow learning student.	2. The concerned faculty identified list slow learning students and the students who are not attending the classes regularly.	2. The related notes and study material is distributed to students for revising.	2. Difficult in understanding the concepts.	2. To take the remedial classes to the slow learners and irregular students.
3. To assess the understandability of the student.	3. Faculty identified few students who were not active in the classroom.	3. Faculty is keenly observing the students in the classroom.	3. Few students were not active in the classroom.	3. Conduct Quizzes and slip tests to assess the students.
4.To identify and discuss content beyond the syllabus.	4. Students Requested for more practical applications.	4. Subject having practical significance in modern applications.	4. Students are not familiar with modern applications.	4. To take extra hours to discuss the practical applications.

  
Member 1

  
Member 2

  
Coordinator

  
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SECOND COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22

Department	ECE	Year/Semester	VII Sem
Course Name	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	Course Code	1804702

S.No	Name	Role	Designation	Signature
1.	Sri A. Valli Bhasha	Coordinator	Assistant Professor	A.V. Bhasha
4.	Sri M. Prabhakar	Member	Assistant Professor	M. Prabhakar
3.	Sri S. Sudheer Kumar	Member	Assistant Professor	SSK

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To Understand the principle of different types of oscilloscopes and use of AC and DC bridges for relevant parameter measurement.	1. Students facing difficulty in understanding AC and DC parameter measurements using bridges	1. Usage of pictures explaining Topics	1. Student must have basics in Electronic Devices and Linear integrated circuit analysis subject for understanding this subject	1. To take extra classes to revise the concepts
2. To check the Coverage of syllabus according to the Internal Examinations.	2. The concerned faculty identified few students are not attending regularly.	2. The related notes and study material is distributed to students for revising.	2. Difficult in understanding the concepts	2. To utilize e-content for explaining more number of problems.

Member	Coordinator	Prof. HOD & H.O.D.



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SECOND COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22

Department	ECE	Year/Semester	IV/VII
Course Name	Digital Image and Video Processing	Course Code	1804710

Committee Members:

S.No	Name	Role	Designation	Signature
1.	MdMahaboob Pasha	Coordinator	Asst. Prof	
2.	A ValliBhasha	Member	Asst. Prof	
3.	M Preethi	Member	Asst. Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To assess the improvements with respect to first CRC  2. To check the Coverage of syllabus according to the Internal Examinations.  3. To identify and sort out any problems in understanding the subject	1. . Students facing difficulty in understanding mathematical analysis  2. The concerned faculty identified few students are not attending regularly.  3. Students requested for providing sufficient number of text books in library for video processing.	1. . Assignments are given to students for revising the topics  2. The related notes and videos of nptel are shared to the students.  3. Quizzes and slip tests conducted.	1. Syllabus is little bit vast at this level and number of classes are less due to more holidays  2. Some students does not have resources for e content and video	1. To take tutorial classes to revise the problems.  2. To utilize e-content for explaining more number of problems in the class using projector  3. To revise the syllabus based on other established universities

Member	Member	Coordinator	HOD Professor & H.O.D.

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**SECOND COURSE REVIEW COMMITTEE REPORT(CRC) 2020-21**

Department	ECE	Year/Semester	IV B.Tech/VII
Course Name	CMOS Design	Course Code	1804706

**Committee Members:**

S.No	Name	Role	Disgnation	Signature
1.	Smt. S. SharmilaBanu	Coordinator	Asst. Professor	
2.	Smt. K. DivyaLakshmi	Member	Asst. Professor	
3.	K. Lakshmi Prasanna	Member	Asst. Professor	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1. To provide rigorous foundation in MOS and CMOS digital circuits 2.To train the students in transistor budgets, clock speeds and the growing challenges of power consumption and productivity .	1. Students are new to the CMOS speed and power consumption of the circuits. 2. Some of the students are weak in digital circuits and in the basics of VLSI.	<b>1.The students learnt the basics of MOS transistor at the basic level.</b> <b>2.The easy way of explaining the concepts of designing CMOS circuits based on digital systems.</b>	1.Difficult to analyze the design of circuits using CMOS-from the students point of view..  Difficulty to understand the performace of the circuits related to speed and power.	The practicing sessions are conducted and explained the basics of the designing and the performance of the circuits.

 Smt. K. DivyaLakshmi	 Smt. S. SharmilaBanu	 Dr.G.Hemalatha
Member	Coordinator	HOD Professor & H.O.D. Department of E.C.E.

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**SECOND COURSE REVIEW COMMITTEE REPORT (CRC) 2020-21**

<b>Department</b>	ECE	<b>Year/Semester</b>	IV/VII
<b>Course Name</b>	IOT	<b>Course Code</b>	1804701


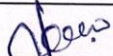
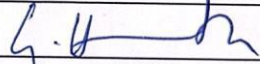
**Committee Members:**

S.No	Name	Role	Disgnation	Signature
1.	Dr. Syed Zahiruddin	Coordinator	Asso.Prof	
2	Sri P. Krishna Teja Yadav	Member	Asst.Prof	
3	Sri. R.V. Suresh	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1.To check the Uniform Coverage of	1.Student wants to listen to teaching in E-class mode and black boards teaching because	1.Usage of <b>E Content</b> , and <b>YOUTUBE</b> videos for explaining	1. Difficult to understand the cloud computing	1. To design IoT applications and real time Projects using MSP processor and Arduino.



<p>syllabus according to the academic calendar.</p> <p>2.To overcome the shortcomings from students' point of view observed in previous meetings</p>	<p>they want to understand the topics elaborately.</p> <p>2. The concerned faculty identified few of them are irregular for classes.</p> <p>3. Students requested for site visit and internship in point of project planning.</p> <p>4. Prescribed Textbooks for act in available in the central library.</p>	<p>each and every topic, by this student is able to understand clearly.</p> <p>2.The related lecture notes hand over before commencement of topics to be covered</p>	<p>concept and Programs related to MSP processor.</p>	<p>2. Explaining more applications related to IoT.</p>
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<p><b>Member</b></p>	<p><b>Coordinator</b></p>	<p><b>HOD</b></p>

Professor & H.O.D.  
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**SECOND COURSE REVIEW COMMITTEE REPORT (CRC) 2021-22**

<b>Department</b>	ECE	<b>Year/Semester</b>	IV/VII
<b>Course Name</b>	IoT lab	<b>Course Code</b>	1804713

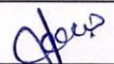

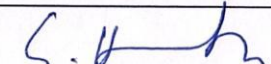
**Committee Members:**

**Date: 04/01/2022**

S.No	Name	Role	Designation	Signature
1.	Sri P. Krishna Teja Yadav	Coordinator	Asst.Prof	
2	Dr. S. Zahiruddin	Member	Asso.Prof	
3	Himaja Reddy	Member	Asst.Prof	

Purpose / Objective	Problems Identified	Result Analysis		Suggestions for Improvement
		Strength	Weakness	
1.To check the Uniform Coverage of experiments	1. Students want to listen the programming fundamentals	Usage of <b>YOUTUBE</b> videos for explaining each and every	Difficult to understand the	1. To create a real time Project using MSP

<p>according to the academic calendar.</p> <p>2. To overcome the shortcomings from students' point of view observed in pervious meetings.</p>	<p>related to MSP processor and its significance.</p> <p>2. The concerned faculty identified a few students are not attending regularly.</p>	<p>experiment.</p> <p>2. The related text books and programs are Sent before commencement of experiments.</p> <p>3. Lab manuals /handouts are given before/after lab.</p>	<p>Programs using MSP processor and Tiva Processor.</p>	<p>processor and Tiva processor for their academic purpose.</p> <p>2. Practicing more experiments.</p>
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<b>Member</b>	<b>Coordinator</b>	<b>HOD</b>

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